	Search Text
1	((plac$8 layout$5) same (latch$3 flip$7) and clock with (delay$4 skew) and center same (mass gravity)).clm.

	Search Text
1	(plac$7 layout$4) same latch$4 same combinational
2	(plac$7 layout$4) same latch$4 same combinational and clock near5 delay$4 and clock near5 tree and clock near5 skew$6
3	(plac$7 layout$4) same latch$4 same (combinational logic adj gate) and clock near5 delay$4 and clock near5 tree and clock near5 skew$6
4	(plac$7 layout$4) same clock adj tree and clock near5 delay$4 and clock near5 skew$6
5	((plac$7 layout$4) same clock adj tree and clock near5 delay$4 and clock near5 skew$6) and latch$4 and (combinational logic adj gate)
6	(((plac$7 layout$4) same clock adj tree and clock near5 delay$4 and clock near5 skew$6) and latch$4 and (combinational logic adj gate)) and (wirelength wir$4 adj length length near4 (connect$4 interconnect))
7	clock same delay$4 same (mimim$6 reduc$4 decreas$4)
8	((((plac$7 layout$4) same clock adj tree and clock near5 delay$4 and clock near5 skew$6) and latch$4 and (combinational logic adj gate)) and (wirelength wir$4 adj length length near4 (connect$4 interconnect))) and (clock same delay$4 same (mimim$6 reduc$4 decreas$4))
9	"5235521".pn. and ((((plac$7 layout$4) same clock adj tree and clock near5 delay$4 and clock near5 skew$6) and latch$4 and (combinational logic adj gate)) and (wirelength wir$4 adj length length near4 (connect$4 interconnect))) and (clock same delay$4 same (mimim$6 reduc$4 decreas$4))
10	(plac$7 layout$4) same latch$4 same (combinat$4 logic adj gate) and clock near5 delay$4 and clock near5 tree and clock near5 skew$6
11	(plac$7 layout$4) same latch$4 same (combinat$4 logic adj gate) and clock near5 delay$4 and clock near5 tree and clock near5 skew$6 and length
12	clock near5 delay$4 and clock near5 tree and clock near5 skew$6 and length and @ad<"20030717" and initial adj2 plac$6
13	clock near5 delay$4 and clock near5 tree and clock near5 skew$6 and length and @ad<"20030717" and initial adj2 plac$6 and clock near5 synthes$5
14	clock near5 delay$4 and clock near5 tree and clock near5 skew$6 and length and @ad<"20030717" and initial adj2 plac$6 and clock near5 synthes$5 and clock same delay$4 same (mimim$6 reduc$4 decreas$4 optim$7)
15	clock near5 delay$4 and clock near5 tree and clock near5 skew$6 and length and @ad<"20030717" and quadratic
16	clock near5 delay$4 and clock near5 tree and clock near5 skew$6 and length and @ad<"20030717" and quadratic and (latch$4 flip$6)
17	(plac$7 layout$4) same latch$4 same combinat$8 and @ad<"20030717" and clock near5 delay$5 and clock near3 skew
18	(plac$7 layout$4) same latch$4 same combinat$8 and @ad<"20030717" and clock near5 delay$5 and clock near3 skew and "716"/$.ccls.